

SN: 09/931,848

IN THE DRAWINGS

Please replace Fig. 3 as indicated in the attached paper entitled "SUBMISSION OF PROPOSED DRAWING AMENDMENT".



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ori Galzur et al.

Assignee: Tower Semiconductor Ltd.

Title: NON-VOLATILE MEMORY DEVICE WITH SELF TEST

Serial No. 09/931,848-4561

File Date: August 16, 2001

Examiner: J. J. Tabone, Jr.

Art Unit: 2133

Atty. Dkt. No.: TSL-097

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Technology Center 2100

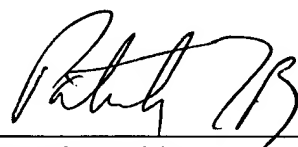
Date: August 3, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**SUBMISSION OF PROPOSED DRAWING AMENDMENT
FOR APPROVAL BY EXAMINER
(37 C.F.R. section 1.123)**

Attached please find a copy of the original Figure 3 with a red ink marking, showing the proposed change to the drawing in this application, for which the approval of the Examiner is requested. A new Figure 3 without a red ink marking is also enclosed for substitution of the originally filed Figure 3.

Date: August 3, 2004

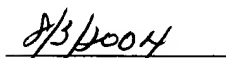


Signature of Practitioner


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I hereby certify that this correspondence is being deposited with the United States Postal Service as FIRST CLASS MAIL in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-1450.



Date



Signature: Rebecca A. Baumann

